

~~〔書類名〕~~ 図面
~~〔図1〕~~
Fig. 1

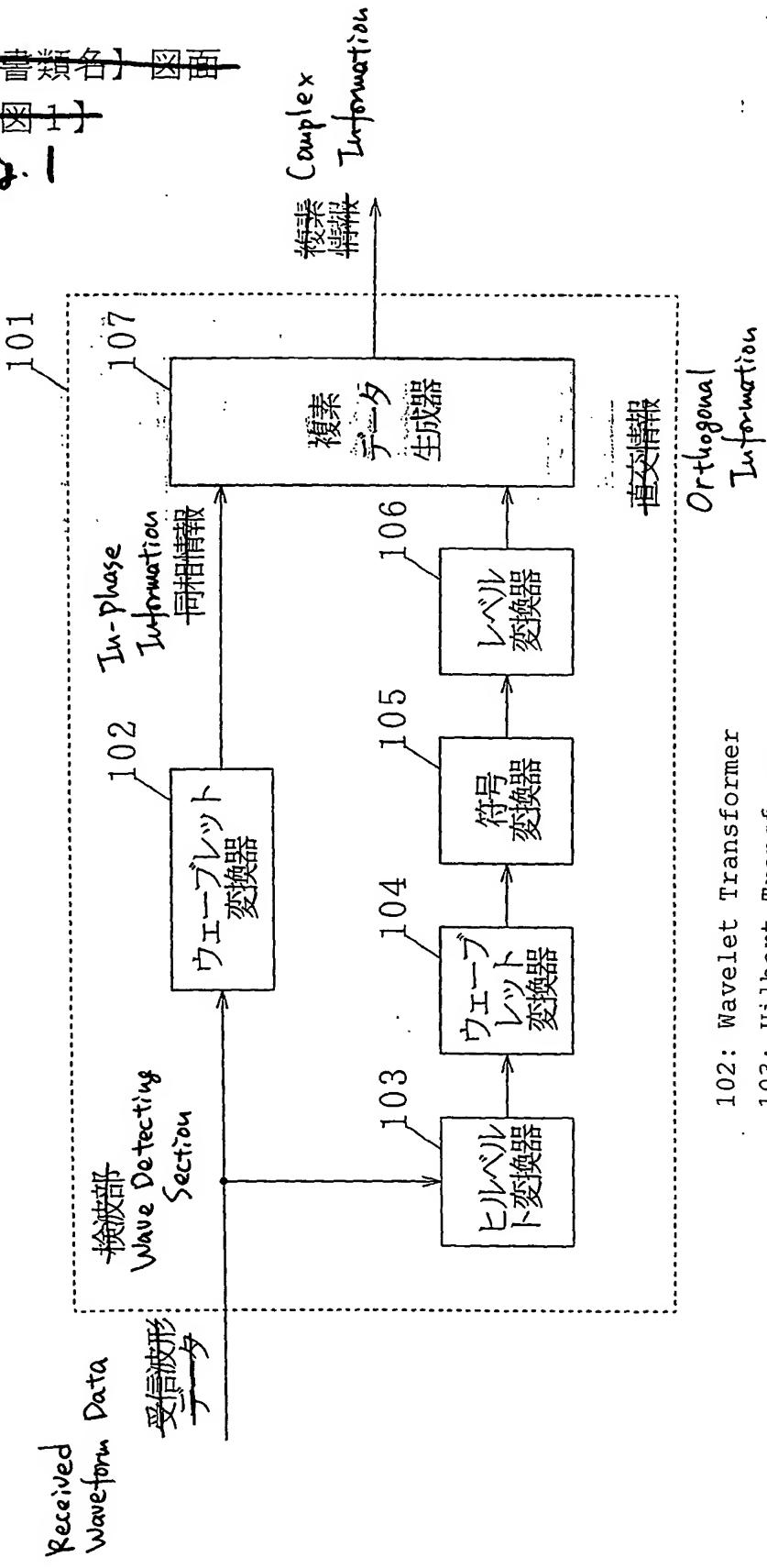
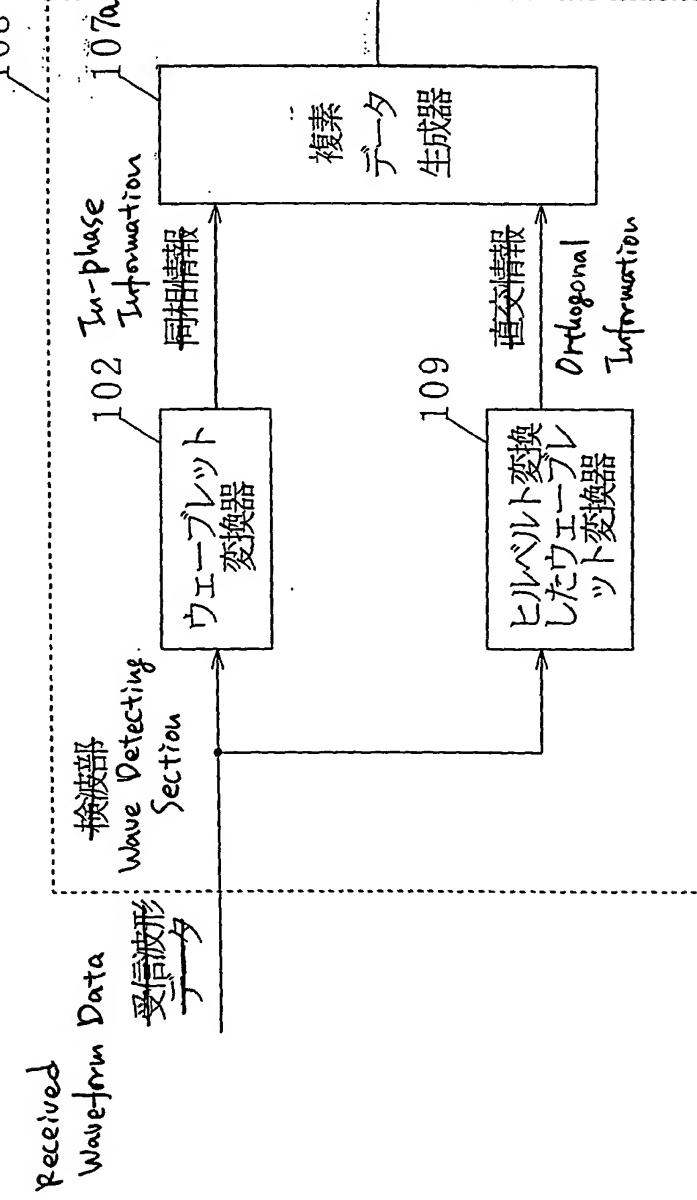


図2

Fig. 2



102: Wavelet Transformer
107: Complex Data Generator
109: Wavelet Transformer Performed Hilbert Transform

図3
Fig. 3

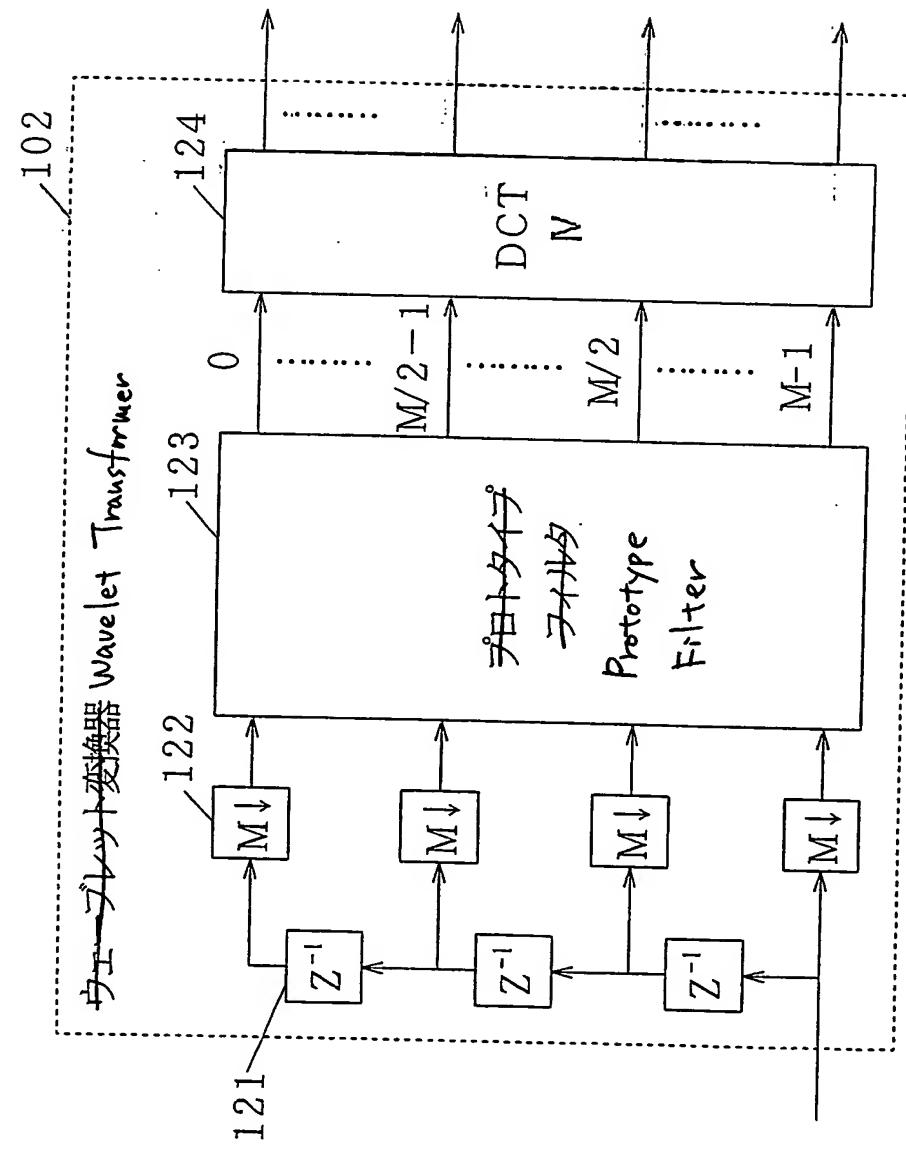


Fig. 4

图 4

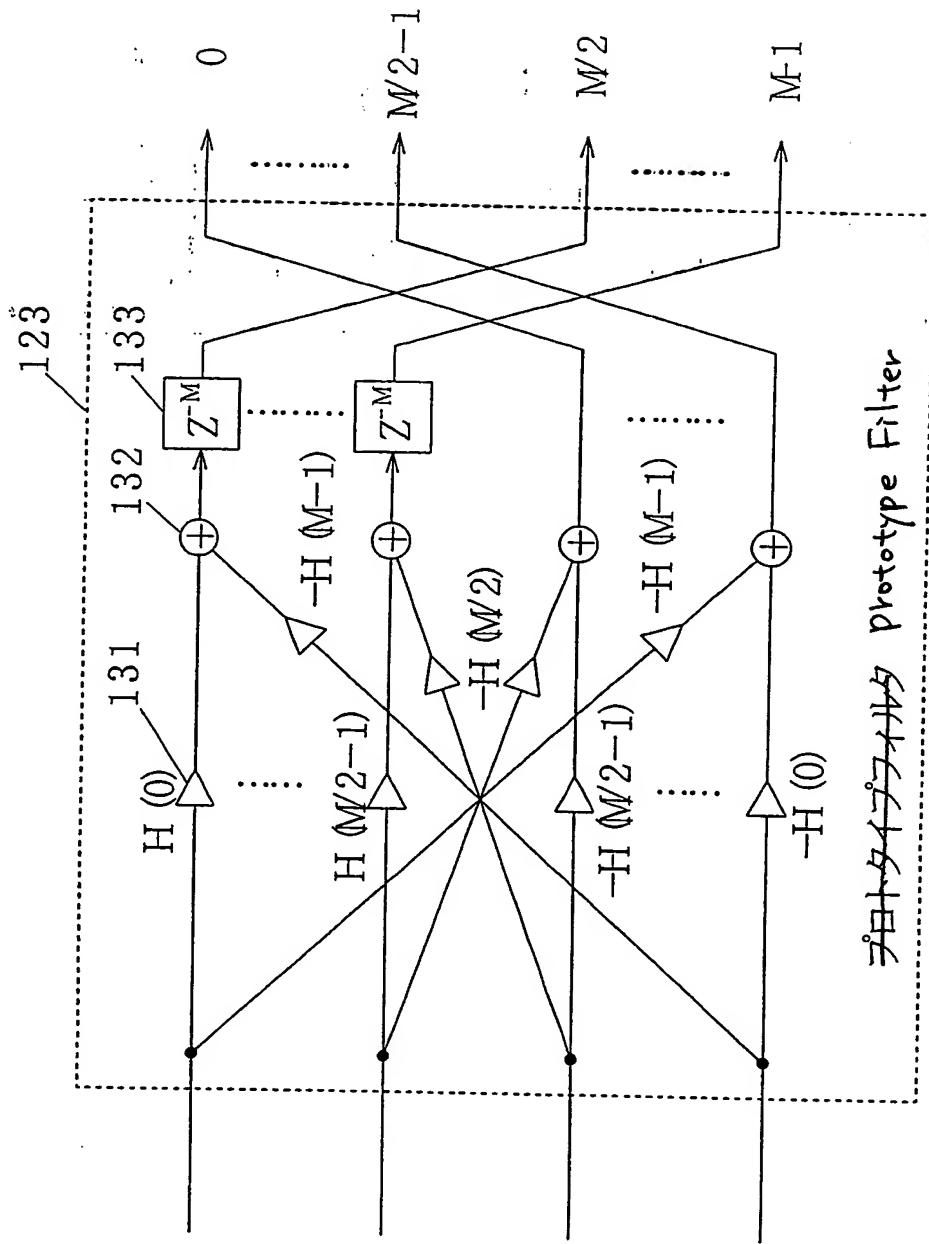


Fig. 5

図5

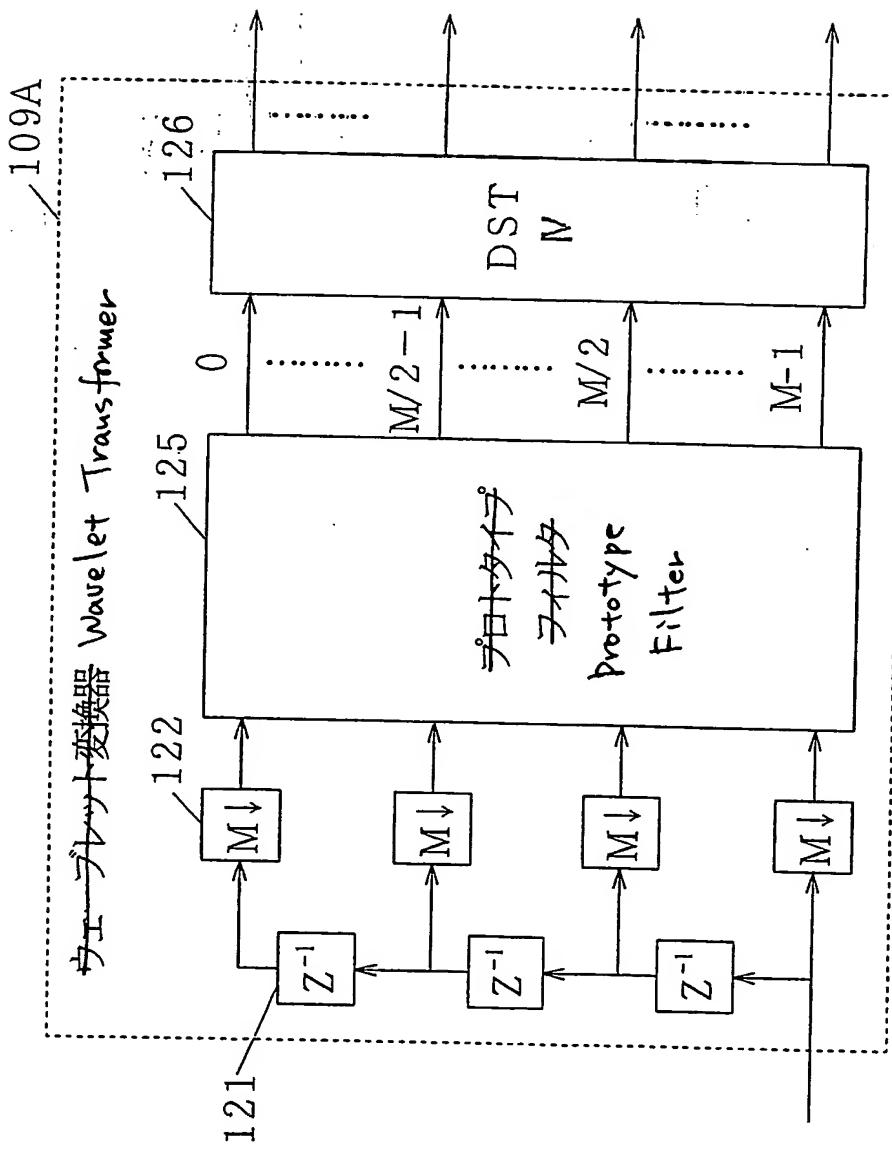


Fig. 6

図 6

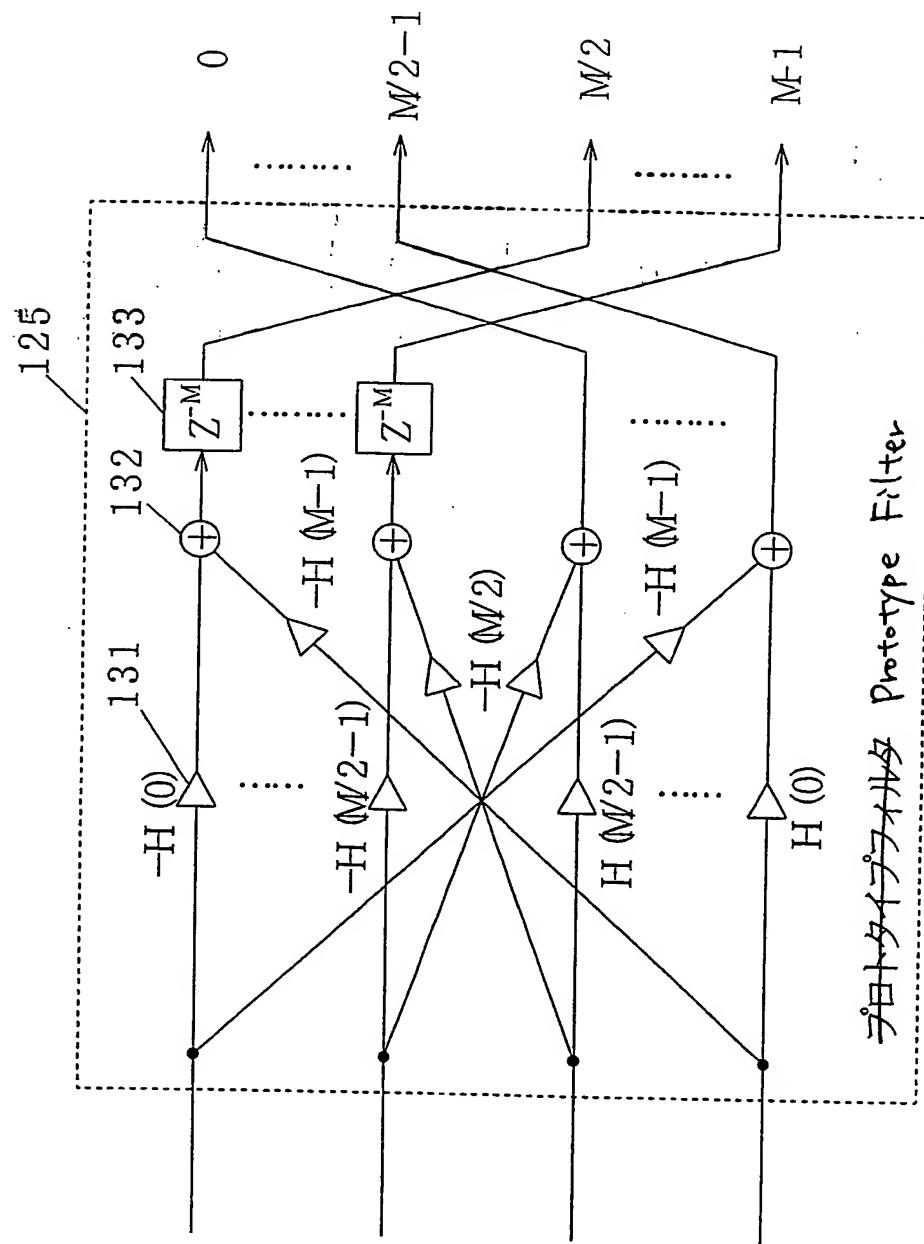
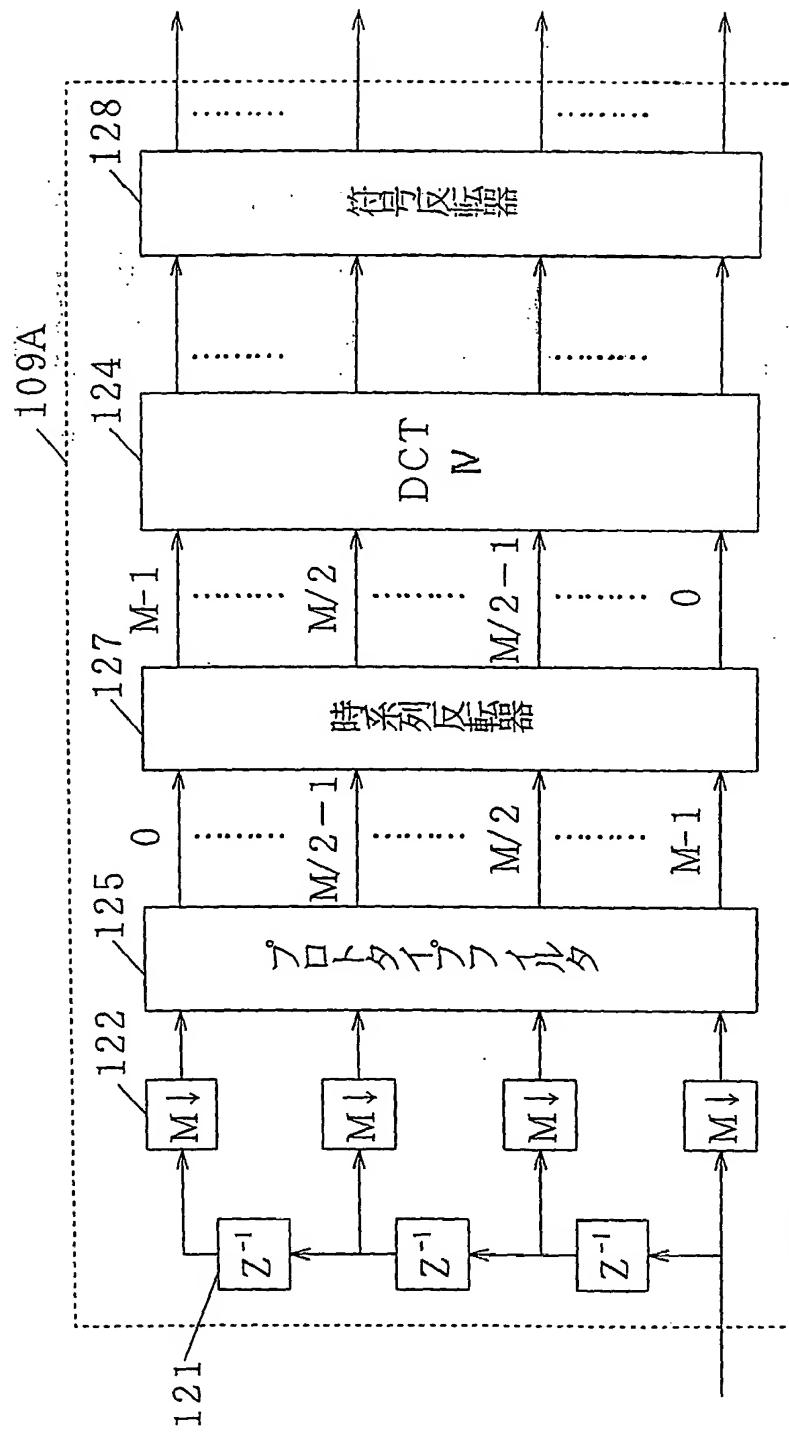


Fig. 7

図 7

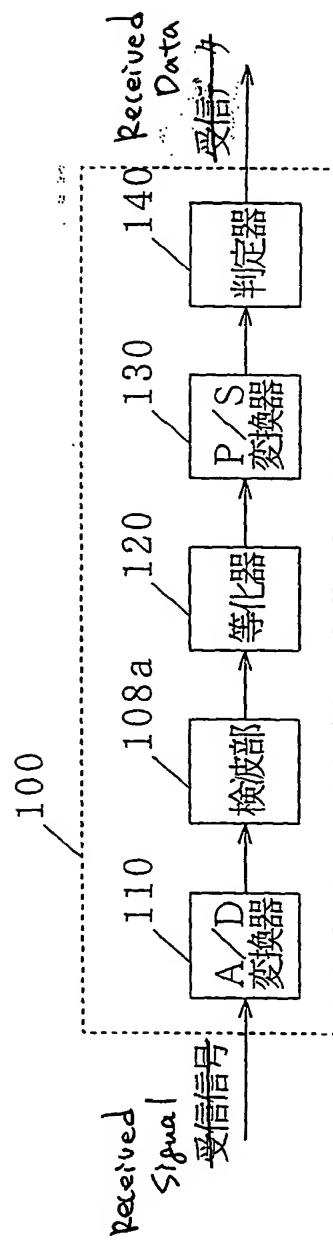


125: Prototype Filter

127: Time Series Inverter

128: Code Converter

Fig. 8
图 8



110: A/D Converter
108a: Wave Detecting Section

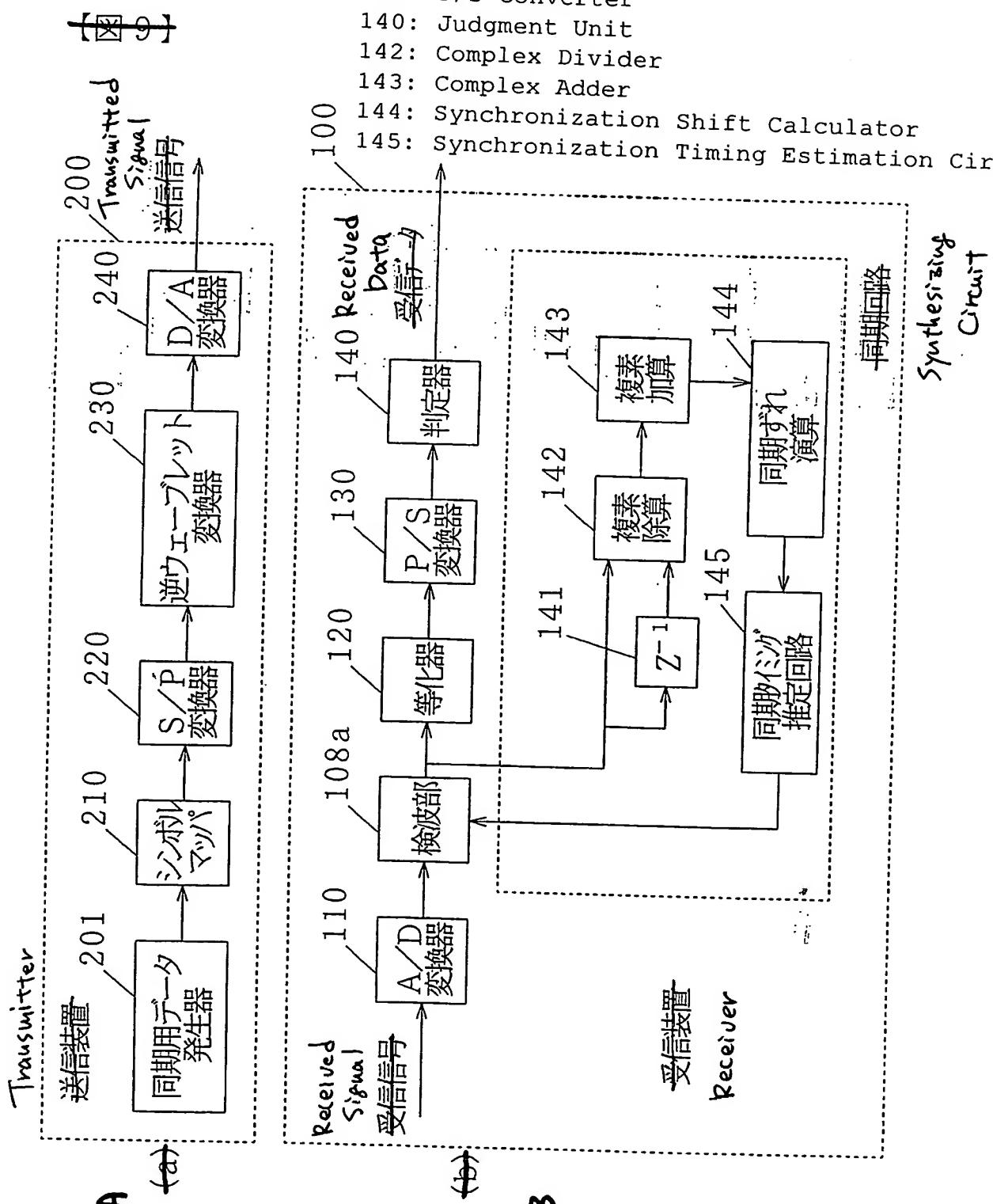
120: Equalizer

130: P/S Converter

140: Judgment Unit

201: Synchronization Data Generator
 210: Symbol Mapper
 220: S/P Converter
 230: Inverse Wavelet Transformer
 240: D/A Converter

108a: Wave Detecting Section
 110: A/D Converter
 120: Equalizer
 130: P/S Converter
 140: Judgment Unit
 142: Complex Divider
 143: Complex Adder
 144: Synchronization Shift Calculator
 145: Synchronization Timing Estimation Circuit



Transmitter

Fig. 9A

受信装置
Receiver

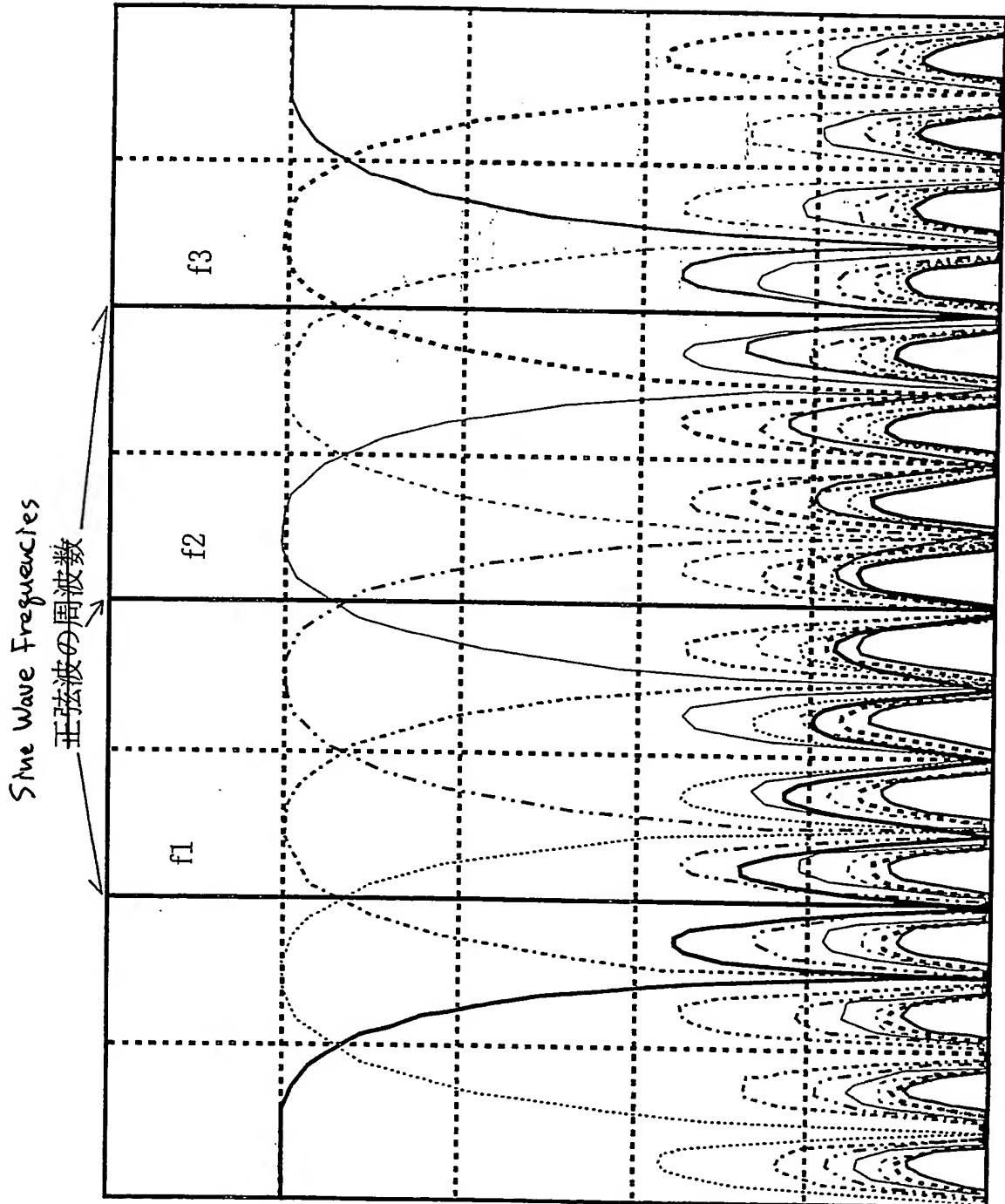
Fig. 9B

同期回路

Synchronizing
Circuit

Fig. 10

10]



#ブキャリア番号左側から0、1、…、7

Subcarriers are numbered from 0 to 7 starting on left side.

Fig. 11
[図11]

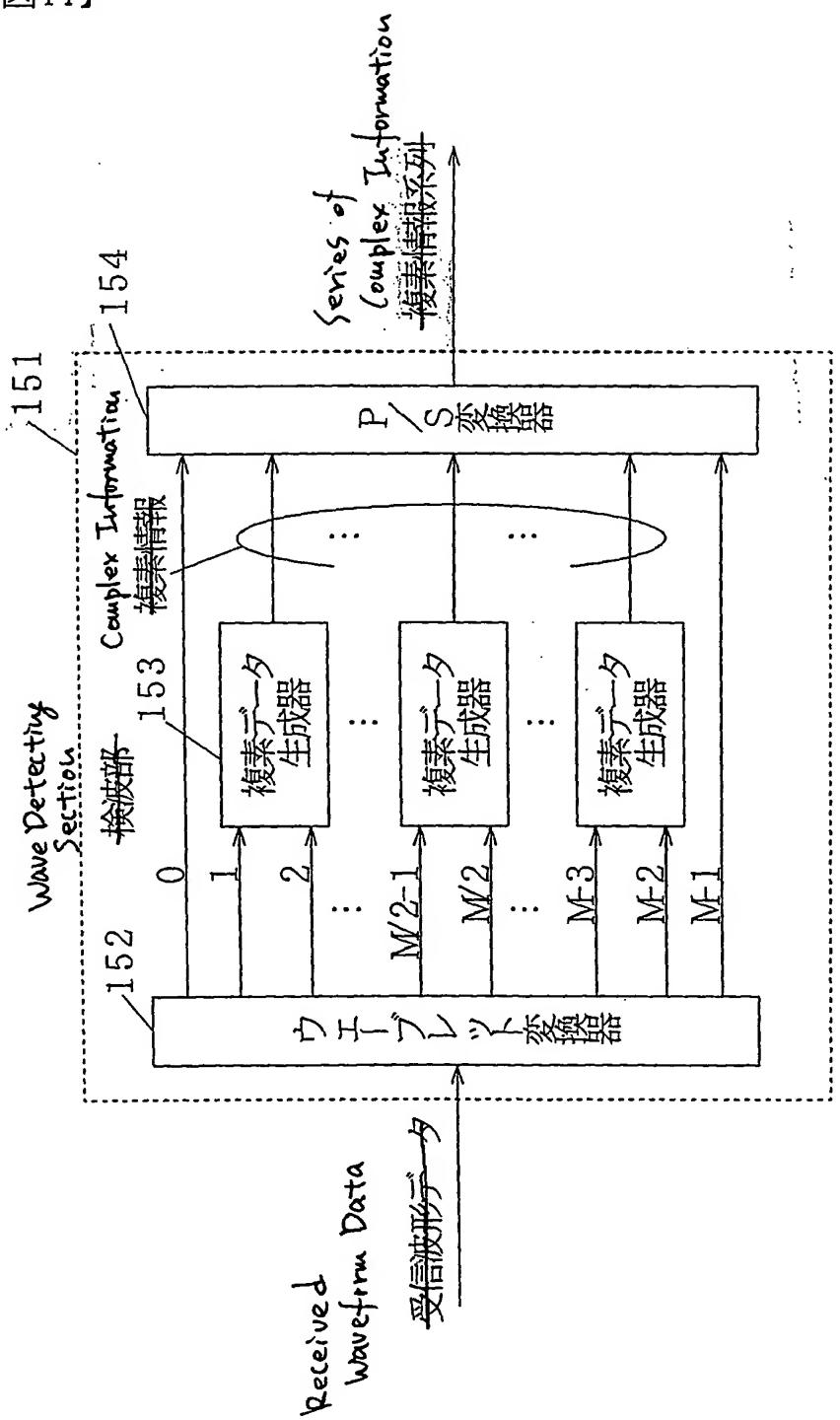
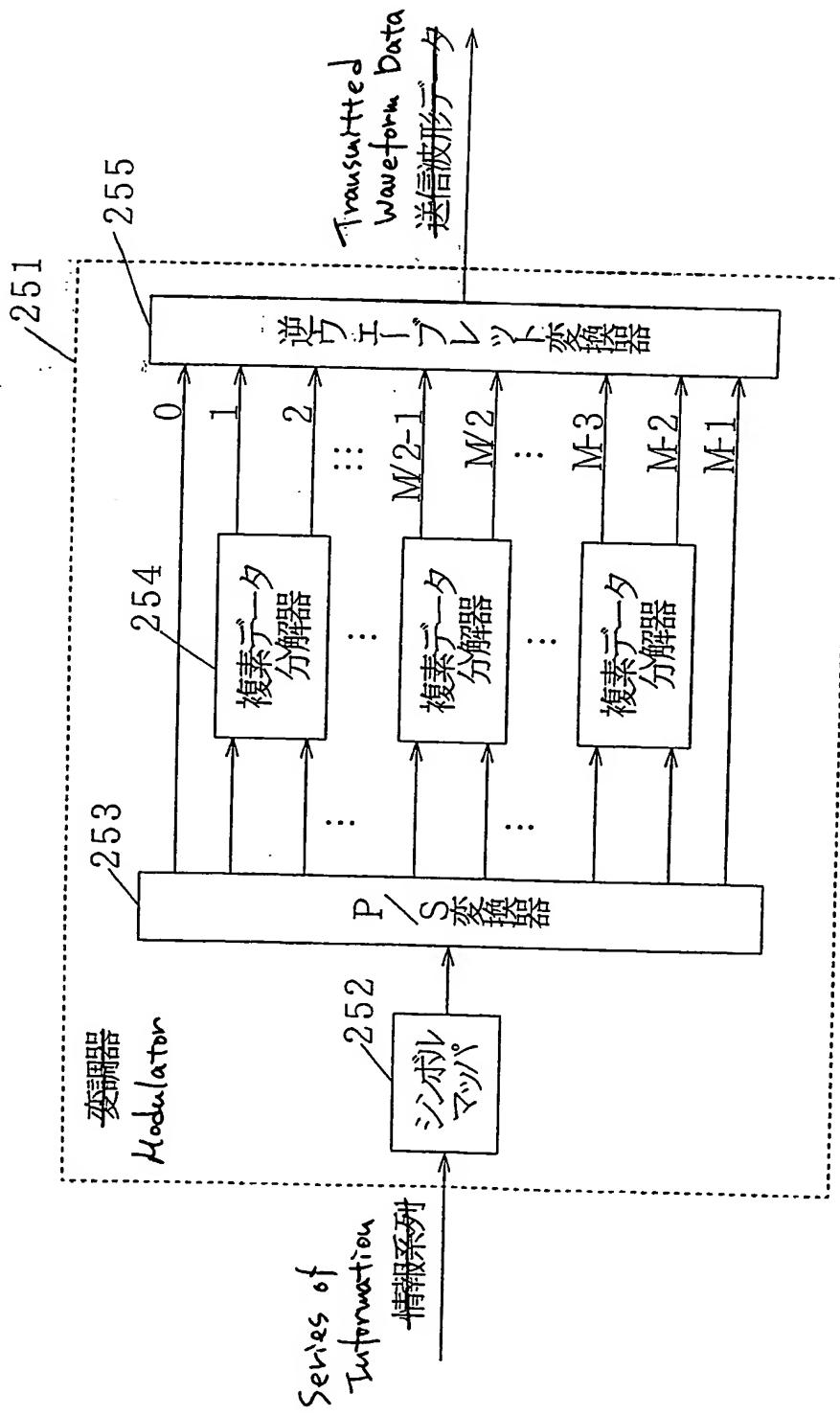


Fig. 12
図12

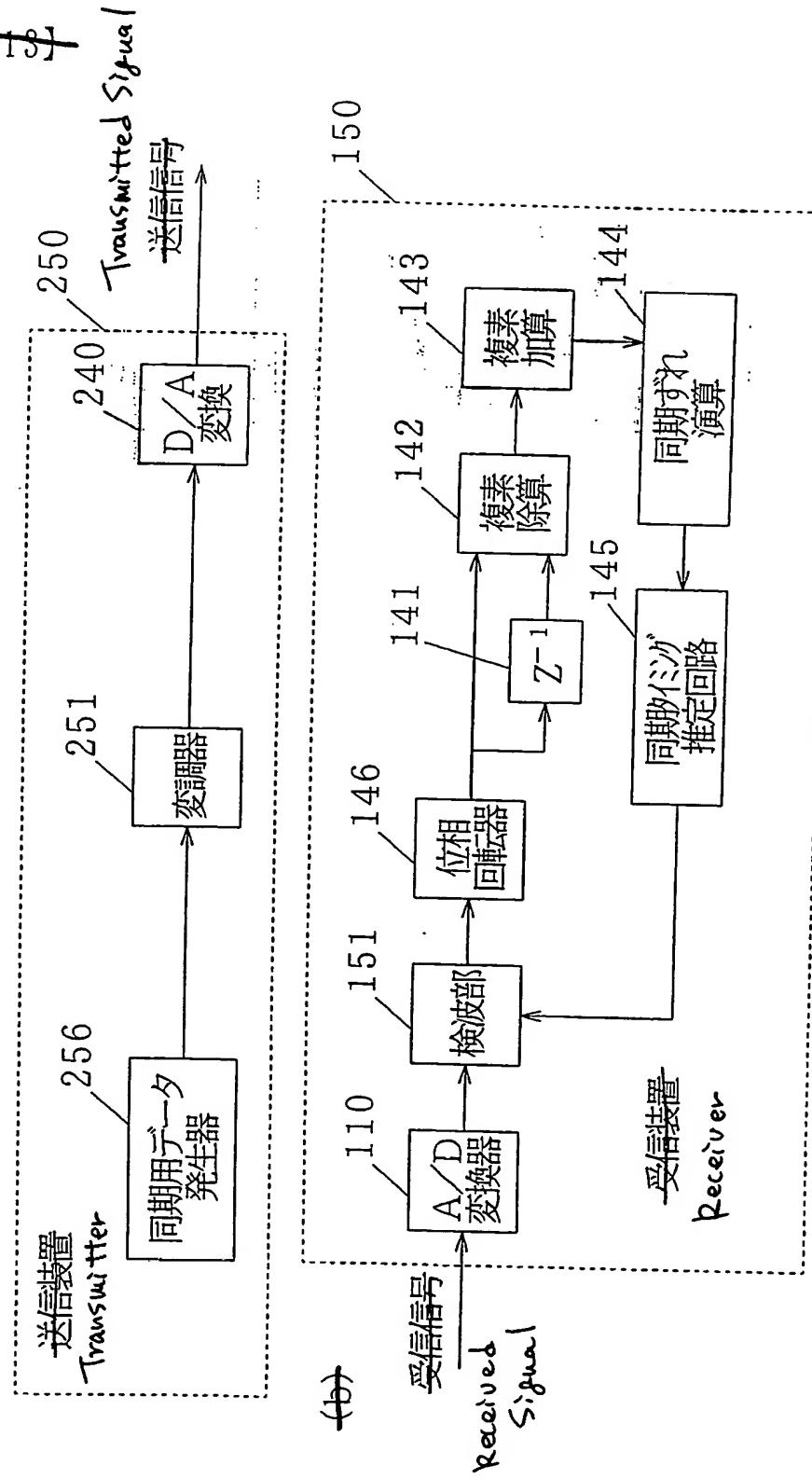
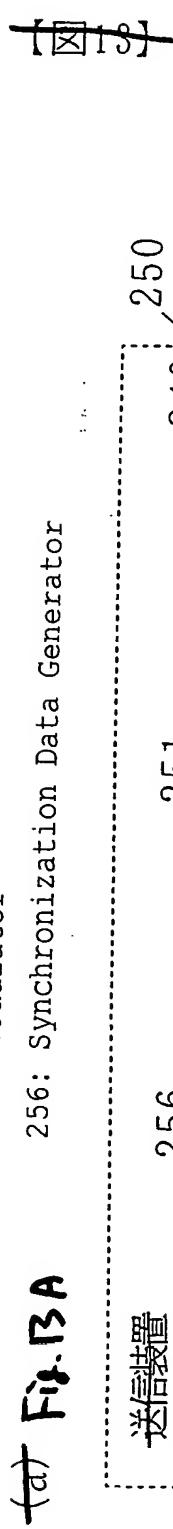


- 252: Symbol Mapper
- 253: P/S Converter
- 254: Complex Data Decomposer
- 255: Inverse Wavelet Transformer

240: D/A Converter

251: Modulator

256: Synchronization Data Generator



110: A/D Converter

142: Complex Divider

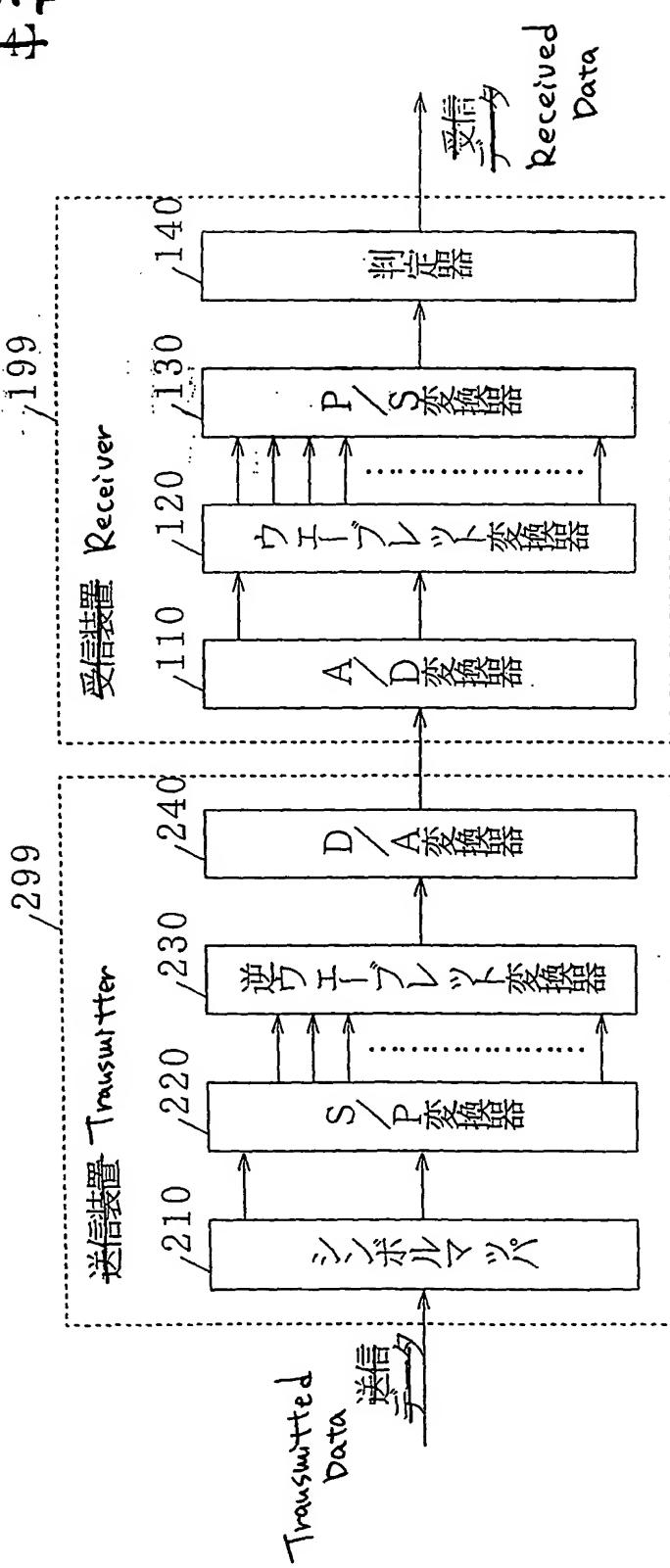
143: Complex Adder

144: Synchronization Shift Calculator

145: Synchronization Timing Estimation Circuit

151: Wave Detecting Section

図 14

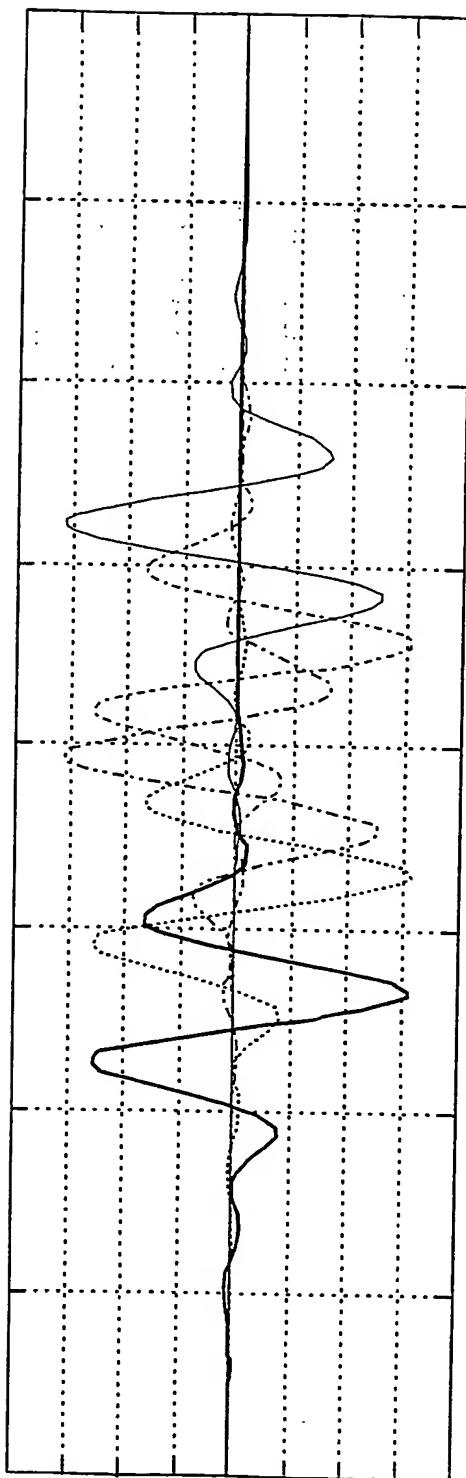


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- 110: A/D Converter
- 120: Wavelet Transformer
- 130: P/S Converter
- 140 Judgment Unit
- 210: Symbol Mapper
- 220: S/P Converter
- 230: Inverse Wavelet Transformer
- 240: D/A Converter

~~图~~ 15

Fig. 15



~~图~~ 16

Fig. 16

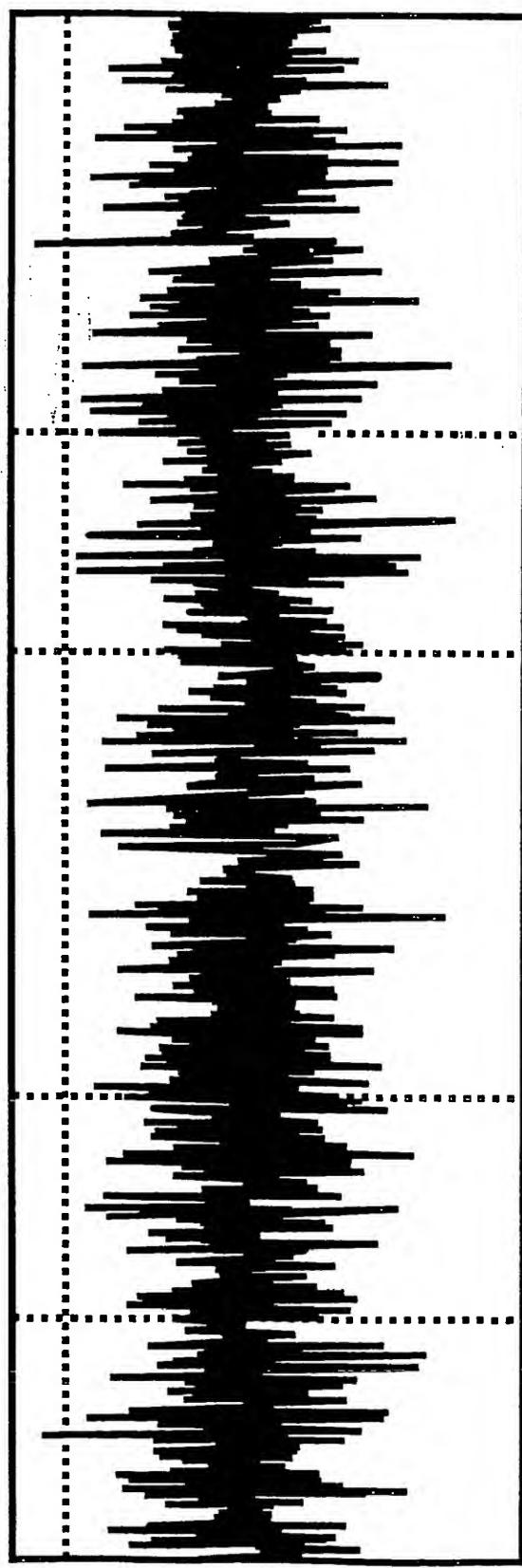


Fig. 17

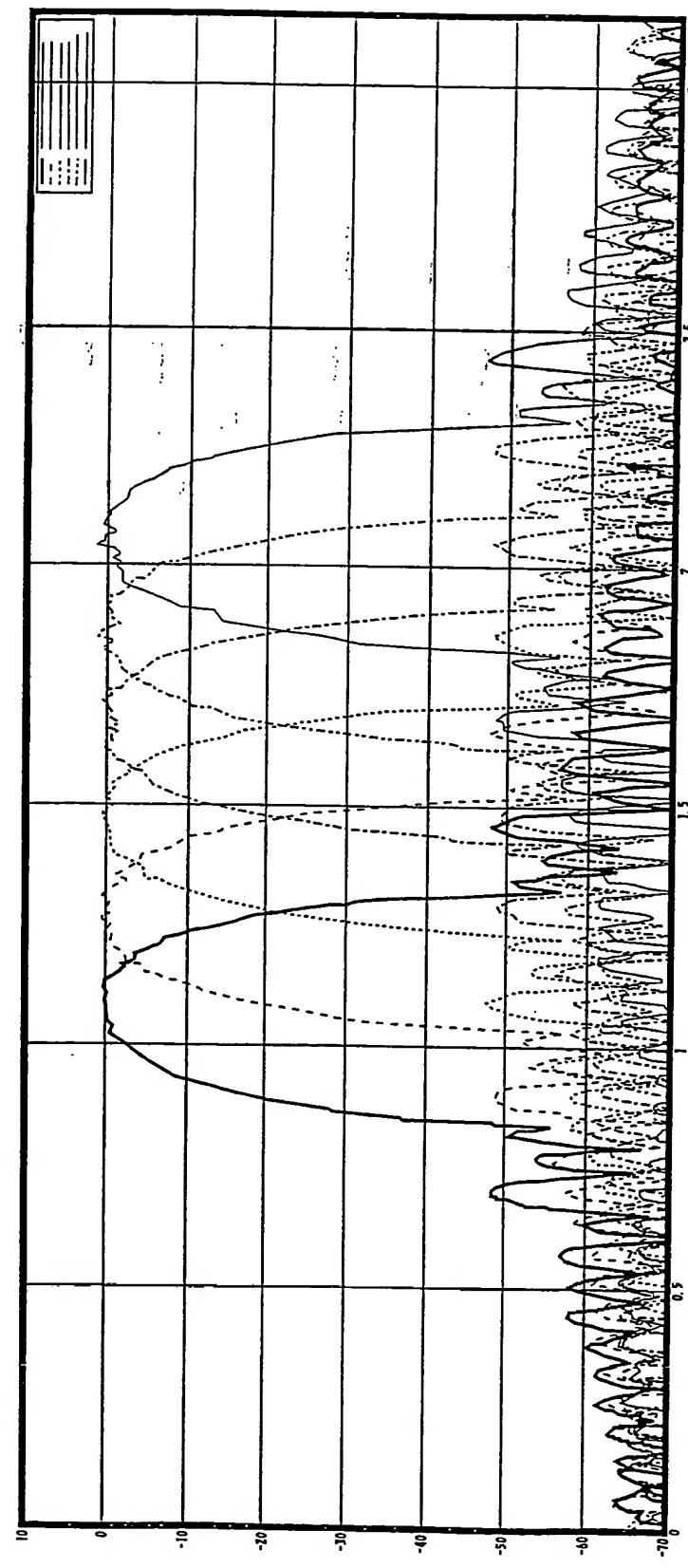


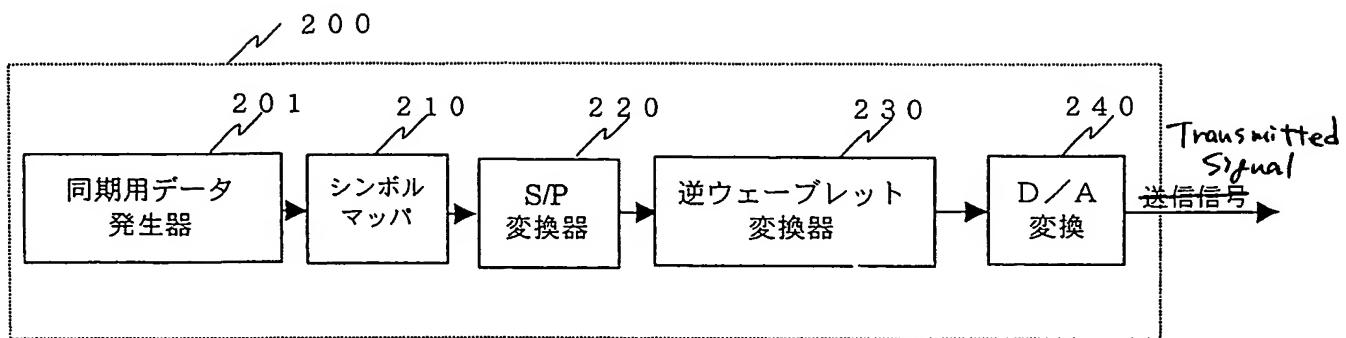
図18

Fig. 18

Synchronization	Equalization	Symbol Transformation	Symbol
同期用	等化用	符号変換用	符号
同期用	等化用	符号変換用	符号
同期用	等化用	符号変換用	符号

Fig.19

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201: Synchronization Data Generator

210: Symbol Mapper

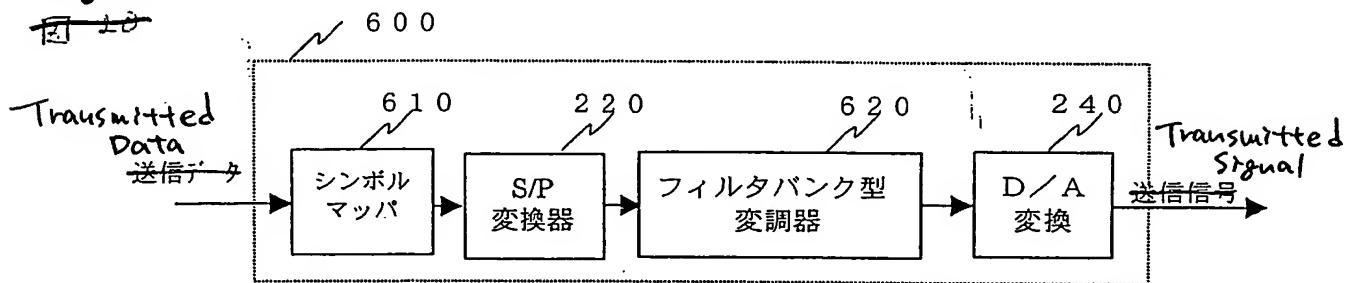
220: S/P Converter

230: Inverse Wavelet Transformer

240: D/A Converter

Fig.20

回20



220: S/P Converter

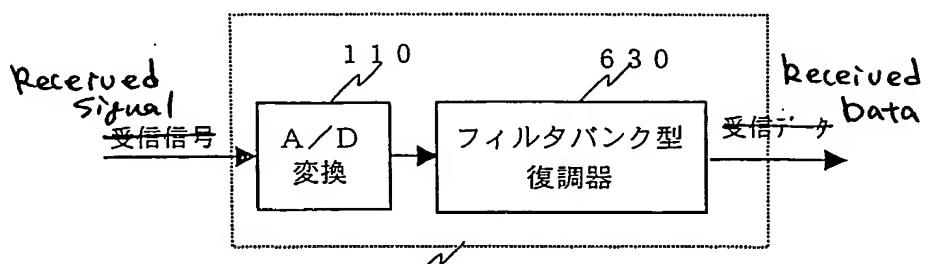
240: D/A Converter

610: Symbol Mapper

620: Filter Bank Type Converter

Fig.21

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110: A/D Converter

630: Filter Bank Type Demodulator

~~Fig.22~~

Fig.22

- 700: Communication Apparatus
- 720: Audio Visual Apparatus
- 730: Documentation Apparatus
- 740: Security Apparatus
- 750: Personal Computer
- 760: Home Electric Apparatus
- 810: Power Line

